

**10.** The PCB of claim **1**, wherein the openings comprise a first opening that is recessed from one of the first and second edges of the base substrate, and a second opening that is a through-hole in the base substrate spaced apart from the first opening.

**11.-12.** (canceled)

**13.** The PCB of claim **1**, wherein the openings are in the connection line, and the guard pattern is part of the connection line.

**14.-17.** (canceled)

**18.** The PCB of claim **1**, wherein the openings are on a virtual bending central line connecting the first edge and the second edge.

**19.** The PCB of claim **18**, wherein a plurality of the openings are located toward ends of the bending region from the virtual bending central line, along a direction crossing the virtual bending central line.

**20.** (canceled)

**21.** A printed circuit board (PCB) comprising:

a base substrate having a first edge and a second edge on opposite sides of the base substrate, the base substrate having a bending region that includes at least one pair of openings respectively adjacent to the first edge and to the second edge and facing each other, and mounting regions that extend from opposite ends of the bending region and that include device mounting portions;

a connection line on the base substrate and crossing bending region, the connection line being configured to connect the device mounting portions; and

a guard pattern on at least one of a top surface and a bottom surface of the base substrate along a boundary of at least one of the at least one pair of openings.

**22.** The PCB of claim **21**, wherein the at least one of the at least one pair of openings is a through-hole in the base substrate that extends from the top surface of the base substrate to the bottom surface of the base substrate, and the opening adjacent to the first edge is between the connection line and the first edge, and the opening adjacent to the second edge is between the connection line and the second edge.

**23.** The PCB of claim **21**, wherein the boundary of at least one of the at least one pair of openings is substantially arc shaped, and

the opening adjacent to the first edge is recessed from the first edge, and the opening adjacent to the second edge is recessed from the second edge.

**24.** The PCB of claim **21**, wherein the at least one pair of openings are formed in the connection line, and the guard pattern is integrally formed with the connection line.

**25.** The PCB of claim **21**, wherein the at least one pair of openings comprise a first opening that is recessed from one of the first and second edges of the base substrate and has a boundary that is substantially arc shaped, and a second opening that is a through-hole between the first opening and the connection line, and

the guard pattern includes a first guard pattern along the boundary of the first opening and a second guard pattern along the boundary of the second opening spaced apart from the first guard pattern.

**26.** The PCB of claim **21**, further comprising a side wall pattern that conformably covers an inner side wall of at least one of the at least one pair of openings.

**27.** The PCB of claim **21**, wherein the guard pattern comprises an upper guard pattern on the top surface of the base substrate and a lower guard pattern on the bottom surface of the base substrate, and

a width of the upper guard pattern and a width of the lower guard pattern are substantially equal to each other.

**28.-29.** (canceled)

**30.** The PCB of claim **21**, wherein the guard pattern comprises an upper guard pattern on the top surface of the base substrate and a lower guard pattern on the bottom surface of the base substrate, and a width of the upper guard pattern is greater than a width of the lower guard pattern.

**31.** (canceled)

**32.** A printed circuit board (PCB) comprising:

a base substrate including at least one opening in at least one edge thereof in a bending region configured to be bent;

at least one guard pattern along a boundary of the at least one opening; and

a connection line on the base substrate and crossing the bending region, the connection line electrically connecting at least two portions on opposite sides of the bending region.

**33.** The PCB of claim **32**, wherein the bending region comprises a first opening adjacent to a first edge thereof, and a second opening adjacent to a second edge thereof opposite to the first edge.

**34.** The PCB of claim **32**, wherein the at least one guard pattern comprises a first guard pattern on a top surface of the base substrate and a second guard pattern on a bottom surface of the base substrate.

**35.** The PCB of claim **32**, wherein the at least two portions comprise at least two device mounting portions configured to mount one or more devices, the one or more devices being electrically connected via the connection line.

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